

SYSTEM TIMINGS

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Key to abbreviations used in formulas

- L_A = Length of the A-field
- L_B = Length of the B-field
- L_C = Length of Multiplicand field
- L_I = Length of Instruction
- L_M = Length of Multiplier field
- L_Q = Length of Quotient field
- L_R = Length of Divisor field
- L_S = Number of significant digits in Divisor (Excludes high-order 0's and blanks)
- L_W = Length of A- or B-field, whichever is shorter
- L_X = Number of characters to be cleared
- L_Y = Number of characters back to right-most "0" in control field
- L_Z = Number of 0's inserted in a field
- I/O = Timing for Input or Output cycle
- F_m = Forms movement times. Allow 20 ms for first space, plus 5 ms for each additional space
- T_m = Tape movement times
- Σ = Number of fields included in an operation

OPERATION	OP CODE	FORMULA
Add (no complement)	A	$.0115 (L_I + 3 + L_A + L_B)$
Add (recomplement)	A	$.0115 (L_I + 3 + L_A + 4 L_B)$
Branch	B	$.0115 (L_I + 1)$
Branch if Bit Equal*	W	$.0115 (L_I + 2)$
Branch if Character Equal	B	$.0115 (L_I + 2)$
Branch if Indicator On	B	$.0115 (L_I + 1)$
Branch if Word Mark and/or Zone	V	$.0115 (L_I + 2)$
Clear Storage	/	$.0115 (L_I + 1 + L_X)$
Clear Word Mark	□	$.0115 (L_I + 3)$
Compare	C	$.0115 (L_I + 1 + L_A + L_B)$
Control Carriage	F	$.0115 (L_I + 1) + F_m$
Control Unit	U	$.0115 (L_I + 1) + T_m$
Divide (aver.)*	%	$.0115 (L_I + 2 + 7 L_X L_Q + 8 L_Q)$
Halt	.	$.0115 (L_I + 1)$
Load Characters to A Word Mark	L	$.0115 (L_I + 1 + 2 L_A)$
Modify Address*	#	$.0115 (L_I + 7 \text{ or } 9)$
Move Characters to A or B Word Mark	M	$.0115 (L_I + 1 + 2 L_W)$
Move Characters and Edit	E	$.0115 (L_I + 1 + L_A + L_B + L_Y)$
Move Characters to Record or Word Mark*	P	$.0115 (L_I + 1 + 2 L_A)$
Move Characters and Suppress Zeros	Z	$.0115 (L_I + 1 + 3 L_A)$
Move and Insert Zeros*	X	$.0115 (L_I + 1 + 2 \Sigma L_A + \Sigma L_Z)$
Move Numeric	D	$.0115 (L_I + 3)$
Move Zone	Y	$.0115 (L_I + 3)$
Multiply (aver.)*	@	$.0115 (L_I + 3 + 2 L_C + 5 L_C L_X + 7 L_X)$
No Operation	N	$.0115 (L_I + 1)$

* Special Feature

Form X24-6447-6

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INSTRUCTION FORMAT

The IBM 1401 Data Processing System uses a variable word-length concept; the length of an instruction can vary from one to eight characters.

OP CODE A- or I-ADDRESS B-ADDRESS d-CHARACTER
X XXX XXXX X

Op Code: This is always a single character which defines the basic operation being performed. A word mark is always associated with the operation code position of an instruction. **A-Address:** This always consists of three characters. It can identify the units position of the A-field, or it can be used to select a special unit or feature (tape unit, column binary feature, disk storage, inquiry, etc.).

I-Address: Instructions that can cause program branches use the I-address to specify the location of the next instruction to be executed if a branch occurs.

B-Address: This is a three-character storage address associated with the B-field. It usually addresses the units position of the B-field, but in some operations, such as tape or disk record read and write, it specifies the high-order position of a record storage area.

d-Character: The d-character is used to modify an operation code. It is a single alphabetic, numerical, or special character, positioned as the last character of an instruction. It can be used with instructions of any length.

PROCESSING OVERLAP

A-Address

The hundreds position of the A-address of a tape or input-output unit (not 1405 or 1407) instruction is changed from % to @. The symbol is used to signal an overlap operation with character reader, magnetic tape, paper tape, and data transmission units.

Overlap Mode

The following instructions are used when the system is in the overlap mode and card, printer or serial I/O operations are to be performed.

INSTRUCTION	FUNCTION
K\$	Overlap On
K(I)\$	Overlap On And Branch
K●	Overlap Off
K(I)●	Overlap Off And Branch
K□	Reset Overlap
K(I)□	Reset Overlap and Branch

1405 TIMING

TIMINGS (Model 2)	MAX.	AVG.	MIN.
Disk to Disk	800 ms	600 ms	450 ms
Track to Track	250 ms	175 ms	100 ms
Record to Record, same Track	50 ms	25 ms	

International Business Machines Corporation
Data Processing Division
112 East Post Road
White Plains, N. Y.

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OPERATION CODE	FUNCTION	MNEMONIC	BCD CODE	CARD CODE	OPERATION CODE	FUNCTION	MNEMONIC	BCD CODE	CARD CODE	INSTRUCTION	FUNCTION	MNEMONIC	BCD CODE	CARD CODE
INPUT-OUTPUT CODES														
1	Read a Card	R	1	1	C	Compare	C	CBA21	12-3	L(%UX)(B)d	Read /Write Tape with Word Marks	LCA	d-modifier, R-Rec Tape	
2	Write a Line	W	2	2	E	Move Characters and Edit	MCE	CBA41	12-5	M(%UX)(B)d	Read /Write Tape	MCW	W-Write Tape	
2 □	Write Word Marks		□ is modifier		F	Control Carriage	CC	CBA42	12-6	M(%CX)(B)R	Read Compressed Tape*		(%CX) is address of tape unit	
3	Write-Read	WR	C21	3	H	Store B-Address Register*	SBR	BA8	12-8	P(A)(B)	Move Characters to Record or Group Mark*	MCM	CB421	11-7
4	Punch a Card	P	4	4	K	Select Stacker	SS	CB2	11-2	U(%UX)d	Control Unit	CU	CA4	0-4
4R	Read-Punch Feed*		R is modifier		N	No Operation	NOP	B41	11-5	X(A)(B)	Move and Insert Zeros*	MIZ	CA421	0-7
4(I)R	Read-Punch Feed and Branch*		R is modifier		Q	Store A-Address Register*	SAR	CB8	11-8	COLUMN BINARY				
5	Read-Punch	RP	C41	5	/	Clear Storage	CS	CA1	0-1	FUNCTION				
6	Write-Punch	WP	C42	6	•	Halt	H	BA821	12-3-8	REMARKS				
6R	Write-Read Punch Feed*		R is modifier		#	Modify Address*	MA	821	3-8					
6(I)R	Write-Read Punch Feed and Branch*		R is modifier		CHARACTER AT d FOR B(I)d BRANCH									
7	Write-Read-Punch	WRP	421	7	d	BRANCH ON	d	BRANCH ON		1C	Read Column Binary	C is Modifier		
8	Start Read Feed*	SRF	8	8	b1	Unconditional	R	Carriage Busy*		4C	Punch Column Binary	C is Modifier		
9	Start Punch Feed*	SPF	C81	9	9	Carr. Chan. #9	T	Low Compare B < A*		M(A)(B)A	Move and Binary Decode	A is Modifier		
ARITHMETIC CODES														
A	Add	A	BA1	12-1	C	Sense Switch C*	?	Reader Error if I/O Check		M(A)(B)B	Move Binary Code	B is Modifier		
S	Subtract	S	CA2	0-2	D	Sense Switch D*		Stop Switch OFF		M(%BX)(A)R	Read Binary Tape	%BX is Address of tape unit		
?	Zero and Add	ZA	CBA82	12-0	E	Sense Switch E*	!	Punch Error if I/O Check		M(%BX)(A)W	Write Binary Tape			
I	Zero and Subtract	ZS	B82	11-0	F	Sense Switch F*		Stop Switch OFF		W(I)(B)d	Branch if Bit Equal	BBE is mnemonic		
@	Multiply*	M	C84	4-8	G	Sense Switch G*	≠	Printer Error if I/O Check		DISK STORAGE %FX DISK OPERATION				
%	Divide*	D	A84	0-4-8	K	End of Reel*	@	Carr. Chan. #12		M(%F0)(B)R	Seek Disk	B is Disk Address		
LOGIC OPERATION CODES														
B(I)	Branch	B	BA2	12-2	L	Tape Error*	%	Processing Check with Process Check Switch	OFF	M(%FX)(B)R	Read Disk	X can be 1, 2, or 3		
B(I)d	Branch if Indicator ON		d is modifier		P	Printer Busy*	/	Unequal Compare B ≠ A		M(%FX)(B)W	Write Disk	1 Specifies Single Record		
B(I)(B)d	Branch if Character is Equal		Contents of B compared to d		S	Equal Compare B = A*				L(%FX)(B)R	Read Disk with Word Marks	2 Specifies Full Track		
V(I)(B)d	Branch if WM and/or Zone	BWZ	A41	0-5						L(%FX)(B)W	Write Disk with Word Marks	3 Specifies a Write Disk Check operation M(%F3)(B)W		
MOVE AND LOAD CODES														
D	Move Numerical	MN	BA4	12-4	d	OPERATION	d	BRANCH ON		1407 INQUIRY %TO ADDRESS				
L	Load Character to A Word Mark	LCA	B21	11-3	B	Backspace Tape Record	N	Access Inoperable		M(%T0)(B)R	Read Console Printer	Data from 1407 transferred to B-address		
M	Move Characters to A or B Word Mark	MCW	CB4	11-4	E	Skip and Blank Tape	V	Read /Write Parity Check or Read Back Check Error		M(%T0)(B)W	Write Console Printer	Data at B-address transferred to 1407		
Y	Move Zone	MZ	CA8	0-8	M	Write Tape Mark	W	Wrong-Length Record		L(%T0)(B)R	Read Console Printer with Word Marks	Data from 1407 transferred to B-address with Word Marks		
Z	Move Characters and Suppress Zeros	MCS	A81	0-9	R	Rewind Tape	X	Unequal Address Compare		L(%T0)(B)W	Write Console Printer with Word Marks	Data at B-address transferred to 1407 with Word Marks		
?	Set Word Mark	SW	CA821	0-3-8	U	Rewind Tape and Upload	Y	Any Disk Storage Error Condition		M(%T0)(B)W	Line Space	B is address of a Group Mark with a Word Mark		
□	Clear Word Mark	CW	CBA84	12-4-8	Q	Inquiry Request	*	Inquiry Clear						